

1A, 50V - 600V Super Fast Surface Mount Rectifier

FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low profile Package
- Low power loss, high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

MECHANICAL DATA

- Case: Sub SMA
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.019g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I _F	1	Α		
V_{RRM}	50 - 600	V		
I _{FSM}	30	Α		
T_{JMAX}	150	°C		
Package	Sub SMA			
Configuration	Single die			









Sub SMA



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)										
PARAMETER	SYMBOL	ES 1AL	ES 1BL	ES 1CL	ES 1DL	ES 1FL	ES 1GL	ES 1HL	ES 1JL	UNIT
Marking code on the device		EAL	EBL	ECL	EDL	EFL	EGL	EHL	EJL	
Repetitive peak reverse voltage	V_{RRM}	50	100	150	200	300	400	500	600	V
Reverse voltage, total rms value	$V_{R(RMS)}$	35	70	105	140	210	280	350	420	V
Forward current	I _F	1			Α					
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	I _{FSM} 30				А				
Junction temperature	T_J	T _J - 55 to +150			°C					
Storage temperature	T _{STG} - 55 to +150			°C						



ES1AL – ES1JL Taiwan Semiconductor

THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction-to-lead thermal resistance	$R_{\Theta JL}$	35	°C/W	
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	85	°C/W	

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
Farmer durable as (1)	ES1AL ES1BL ES1CL ES1DL	- I _F = 1A, T _J = 25°C	V _F	-	0.95	V
Forward voltage ⁽¹⁾	ES1FL ES1GL			-	1.30	V
	ES1HL ES1JL			-	1.70	V
Reverse current @ rated V _R ⁽²⁾		T _J = 25°C		-	5	μA
		T _J = 125°C	- I _R	-	100	μA
Lunction conscitones	ES1AL ES1BL ES1CL ES1DL	4MH= \/ 4 0\/		10	-	pF
Junction capacitance	ES1FL ES1GL ES1HL ES1JL	1MHz, V _R = 4.0V	CJ	8	-	pF
Reverse recovery time		$I_{rr} = 0.5A, I_{R} = 1.0A,$ $I_{rr} = 0.25A$	t _{rr}	-	35	ns

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE ⁽¹⁾	PACKAGE	PACKING		
ES1xL	Sub SMA	10,000 / Tape & Reel		

Notes:

1. "x" defines voltage from 50V(ES1AL) to 600V(ES1JL)



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.1 Forward Current Derating Curve

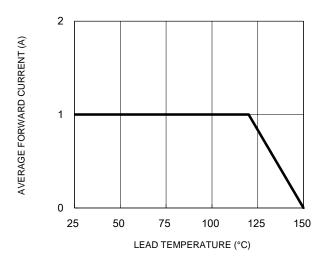


Fig.3 Typical Reverse Characteristics

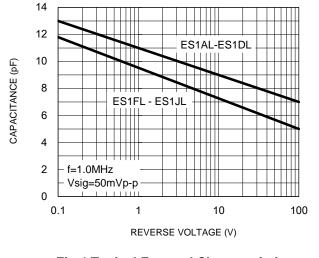
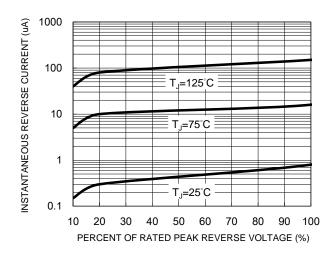


Fig.2 Typical Junction Capacitance

Fig.4 Typical Forward Characteristics



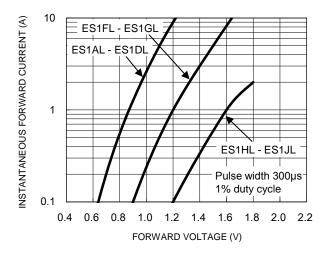
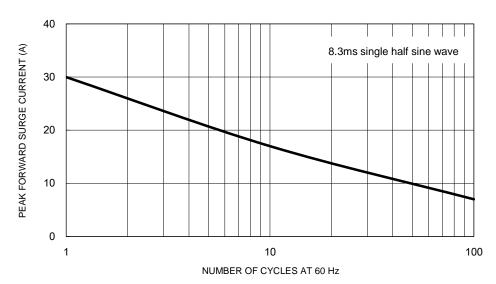


Fig.5 Maximum Non-Repetitive Forward Surge Current

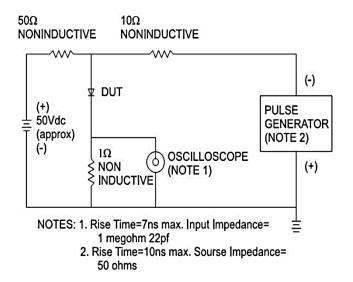


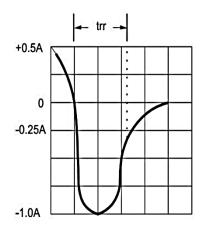


CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram

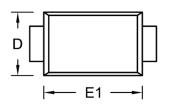


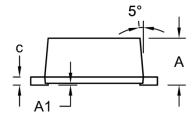


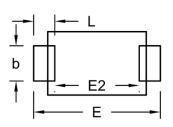


PACKAGE OUTLINE DIMENSIONS

Sub SMA

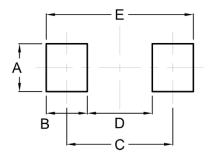






DIM.	Unit (mm)		Unit	(inch)	
DIWI.	Min.	Max.	Min.	Max.	
Α	1.23	1.43	0.048	0.056	
A1	0.00	0.10	0.000	0.004	
b	0.80	1.20	0.031	0.047	
С	0.16	0.30	0.006	0.012	
D	1.70	1.90	0.067	0.075	
E	3.40	3.80	0.134	0.150	
E1	2.70	2.90	0.106	0.114	
E2	2.45	2.60	0.096	0.102	
L	0.35	0.85	0.014	0.033	

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	1.40	0.055
В	1.20	0.047
С	3.10	0.122
D	1.90	0.075
E	4.30	0.169

MARKING DIAGRAM



P/N = Marking Code G = Green Compound

ΥW = Date Code F = Factory Code



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